



2815

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Izvo Iida  
Serial No. : 09/876,554  
Filed : June 7, 2001

Art Unit : 2815  
Examiner : N. Richards

Title : METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

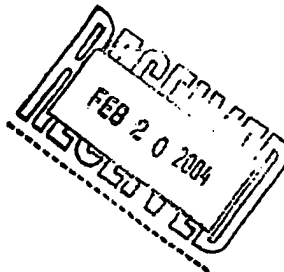
TECHNOLOGY CENTER 2800

FEB 10 2004

RECEIVED

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as indicated on the following pages.



CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit February 3, 2004

Signature Rose Papetti

Typed or Printed Name of Person Signing Certificate Rose Papetti

02/12/2004 AMETH 03323001 061059 09876554

01 FC:1222 54.00 DA

02 FC:1241 172.03 DA

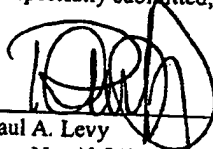
The 35 U.S.C. 112 rejection was addressed by amending claim 16 to recite the limitation  
"selectively etching the tunnel insulating film except from on the region of the semiconductor  
substrate where the MOS transistor is to be formed."

Conclusion

All pending claims are in condition for allowance.

The Applicants do not believe that there are any fees due for this amendment. Please  
apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,



Paul A. Levy  
Reg. No. 45,748

Date: February 3, 2004

Fish & Richardson P.C.  
45 Rockefeller Plaza, Suite 2800  
New York, New York 10111  
Telephone: (212) 765-5070  
Facsimile: (212) 258-2291